



LM110/LM210/LM310

LM110/LM210/LM310 Voltage Follower

General Description

The LM110 series are monolithic operational amplifiers internally connected as unity-gain non-inverting amplifiers. They use super-gain transistors in the input stage to get low bias current without sacrificing speed. Directly interchangeable with 101, 741 and 709 in voltage follower applications, these devices have internal frequency compensation and provision for offset balancing.

The LM110 series are useful in fast sample and hold circuits, active filters, or as general-purpose buffers. Further, the frequency response is sufficiently better than standard IC amplifiers that the followers can be included in the feedback loop without introducing instability. They are plug-in replacements for the LM102 series voltage followers, offer-

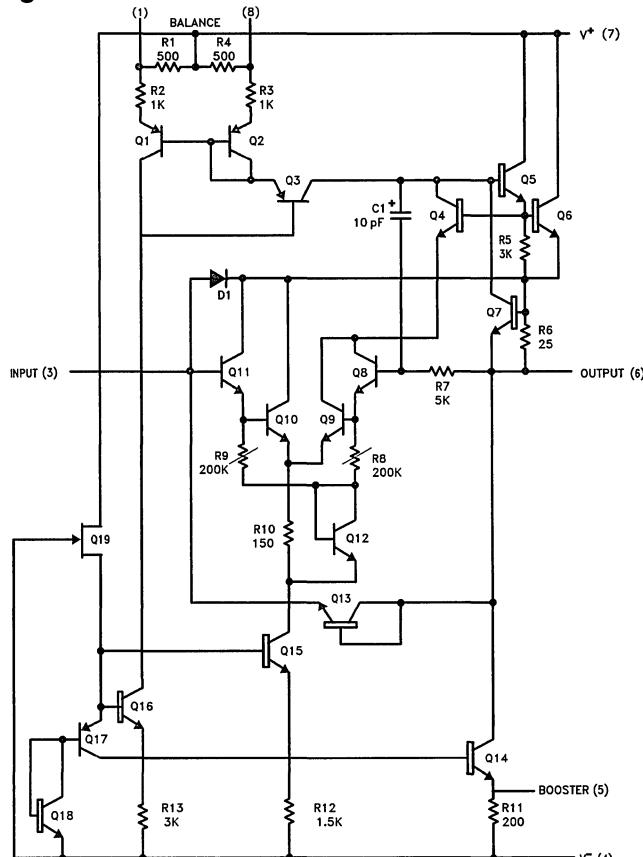
ing lower offset voltage, drift, bias current and noise in addition to higher speed and wider operating voltage range.

The LM110 is specified over a temperature range $-55^{\circ}\text{C} \leq T_A \leq +125^{\circ}\text{C}$, the LM210 from $-25^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ and the LM310 from $0^{\circ}\text{C} \leq T_A \leq +70^{\circ}\text{C}$.

Features

■ Input current	10 nA max over temperature
■ Small signal bandwidth	20 MHz
■ Slew rate	30 V/ μs
■ Supply voltage range	$\pm 5\text{V}$ to $\pm 18\text{V}$

Schematic Diagram



TL/H/7761-1

3

Absolute Maximum Ratings

If Military/Aerospace specified devices are required, contact the National Semiconductor Sales Office/Distributors for availability and specifications.

(Note 6)

Supply Voltage	$\pm 18V$
Power Dissipation (Note 1)	500 mW
Input Voltage (Note 2)	$\pm 15V$
Output Short Circuit Duration (Note 3)	Indefinite
Operating Temperature Range LM110	$-55^{\circ}C$ to $+125^{\circ}C$
LM210	$-25^{\circ}C$ to $+85^{\circ}C$
LM310	$0^{\circ}C$ to $+70^{\circ}C$

Storage Temperature Range $-65^{\circ}C$ to $+150^{\circ}C$
Lead Temperature (Soldering, 10 sec.) $260^{\circ}C$

Soldering Information	Dual-In-Line Package	$260^{\circ}C$
	Soldering (10 sec.)	$260^{\circ}C$
	Small Outline Package	$215^{\circ}C$
	Vapor Phase (60 sec.)	$220^{\circ}C$
	Infrared (15 sec.)	$220^{\circ}C$

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

ESD rating to be determined.

Electrical Characteristics (Note 4)

Parameter	Conditions	LM110			LM210			LM310			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	$T_A = 25^{\circ}C$		1.5	4.0		1.5	4.0		2.5	7.5	mV
Input Bias Current	$T_A = 25^{\circ}C$		1.0	3.0		1.0	3.0		2.0	7.0	nA
Input Resistance	$T_A = 25^{\circ}C$	10^{10}	10^{12}		10^{10}	10^{12}		10^{10}	10^{12}		Ω
Input Capacitance			1.5			1.5			1.5		pF
Large Signal Voltage Gain	$T_A = 25^{\circ}C, V_S = \pm 15V$ $V_{OUT} = \pm 10V, R_L = 8 k\Omega$	0.999	0.9999		0.999	0.9999		0.999	0.9999		V/V
Output Resistance	$T_A = 25^{\circ}C$		0.75	2.5		0.75	2.5		0.75	2.5	Ω
Supply Current	$T_A = 25^{\circ}C$		3.9	5.5		3.9	5.5		3.9	5.5	mV
Input Offset Voltage				6.0			6.0			10	mV
Offset Voltage	$-55^{\circ}C \leq T_A \leq +85^{\circ}C$		6			6					$\mu V/^{\circ}C$
Temperature Drift	$T_A = 125^{\circ}C$ $0^{\circ}C \leq T_A \leq +70^{\circ}C$		12			12			10		$\mu V/^{\circ}C$ $\mu V/^{\circ}C$
Input Bias Current				10			10			10	nA
Large Signal Voltage Gain	$V_S = \pm 15V, V_{OUT} = \pm 10V$ $R_L = 10 k\Omega$	0.999			0.999			0.999			V/V
Output Voltage Swing (Note 5)	$V_S = \pm 15V, R_L = 10 k\Omega$	± 10			± 10			± 10			V
Supply Current	$T_A = 125^{\circ}C$		2.0	4.0		2.0	4.0				mA
Supply Voltage Rejection Ratio	$\pm 5V \leq V_S \leq \pm 18V$	70	80		70	80		70	80		dB

Note 1: The maximum junction temperature of the LM110 is $150^{\circ}C$, of the LM210 is $100^{\circ}C$, and of the LM310 is $85^{\circ}C$. For operating at elevated temperatures, devices in the TO-5 package must be derated based on a thermal resistance of $150^{\circ}C/W$, junction to ambient, or $45^{\circ}C/W$, junction to case. The thermal resistance of the dual-in-line package is $100^{\circ}C/W$, junction to ambient.

Note 2: For supply voltages less than $\pm 15V$, the absolute maximum input voltage is equal to the supply voltage.

Note 3: Continuous short circuit for the LM110 and LM210 is allowed for case temperatures to $125^{\circ}C$ and ambient temperatures to $70^{\circ}C$, and for the LM310, $70^{\circ}C$ case temperature or $55^{\circ}C$ ambient temperature. It is necessary to insert a resistor greater than $2 k\Omega$ in series with the input when the amplifier is driven from low impedance sources to prevent damage when the output is shorted. $R_S = 5k$ min, $14k$ typical is recommended for dynamic stability in all applications.

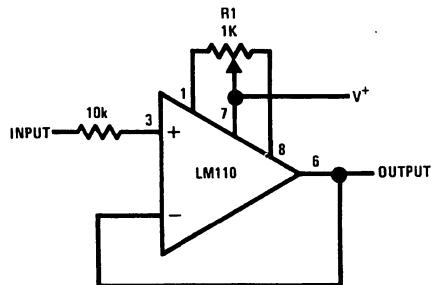
Note 4: These specifications apply for $\pm 5V \leq V_S \leq \pm 18V$ and $-55^{\circ}C \leq T_A \leq 125^{\circ}C$ for the LM110, $-25^{\circ}C \leq T_A \leq 85^{\circ}C$ for the LM210, and $0^{\circ}C \leq T_A \leq 70^{\circ}C$ for the LM310 unless otherwise specified.

Note 5: Increased output swing under load can be obtained by connecting an external resistor between the booster and V^- terminals. See curve.

Note 6: Refer to RETS110X for LM110H, LM110J military specifications.

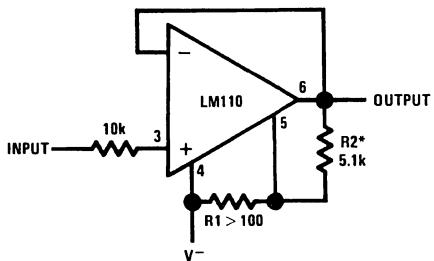
Auxiliary Circuits

Offset Balancing Circuit



TL/H/7761-2

Increasing Negative Swing Under Load

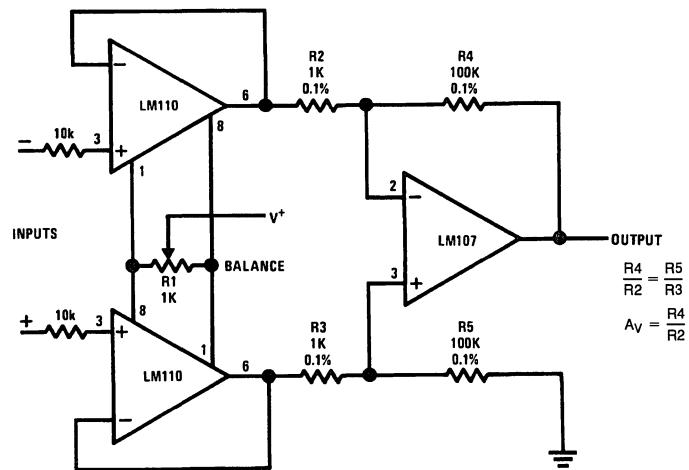


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*May be added to reduce internal dissipation

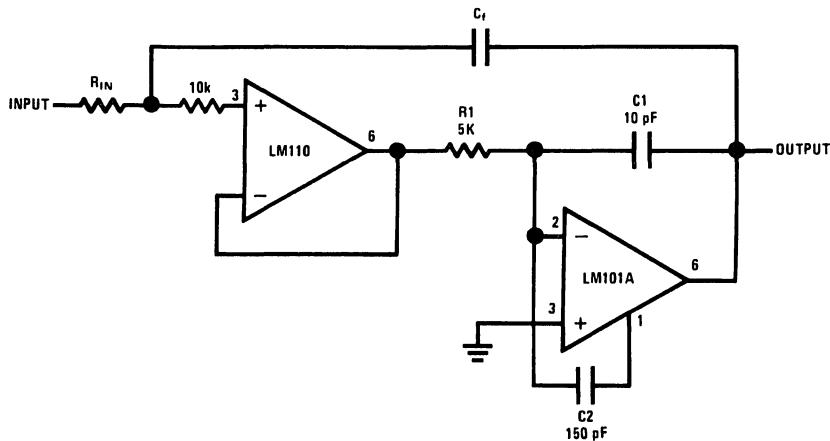
Typical Applications

Differential Input Instrumentation Amplifier

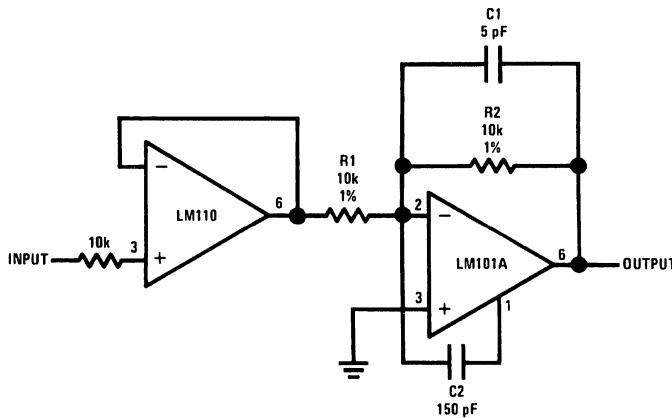


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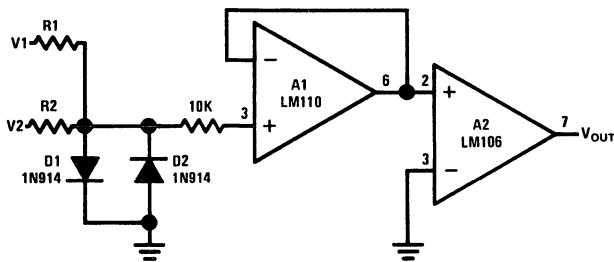
Fast Integrator with Low Input Current



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Typical Applications (Continued)**Fast Inverting Amplifier with High Input Impedance**

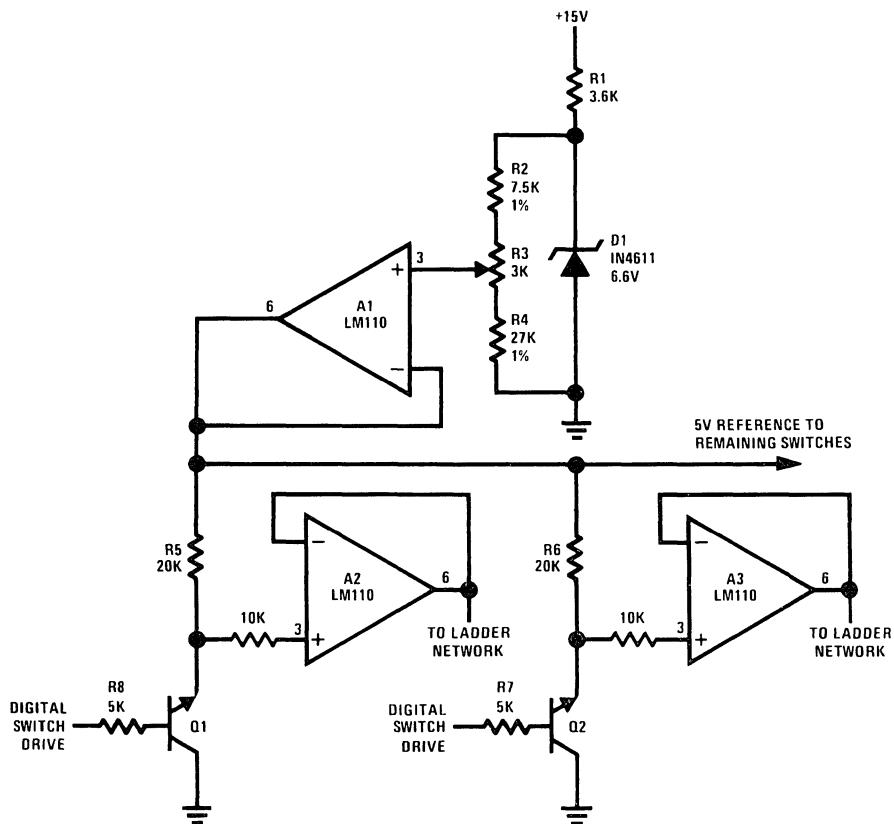
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Comparator for Signals of Opposite Polarity

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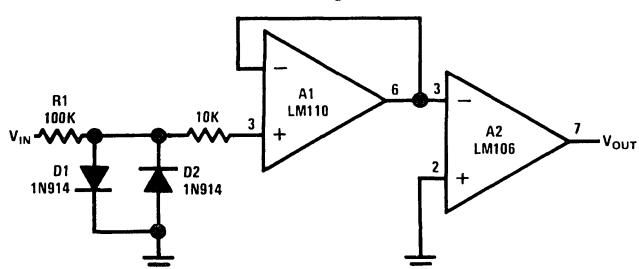
Typical Applications (Continued)

Driver for A/D Ladder Network

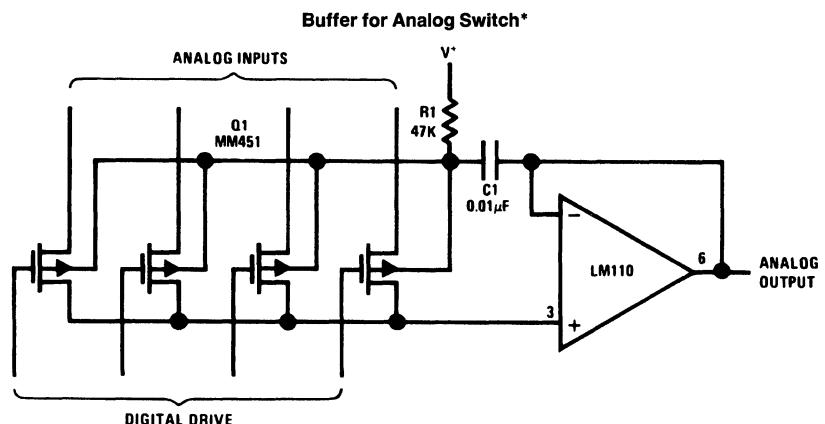


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Zero Crossing Detector

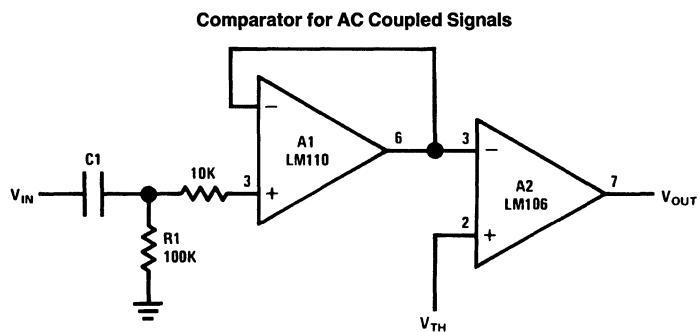


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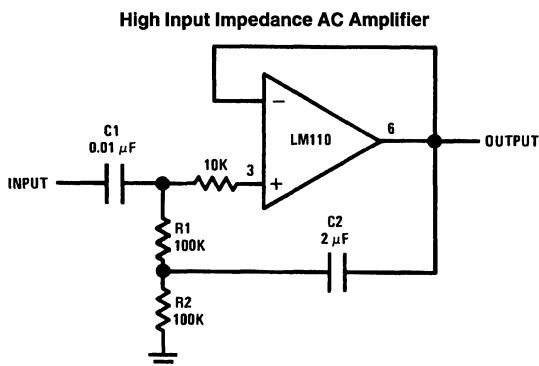
Typical Applications (Continued)

TL/H/7761-10

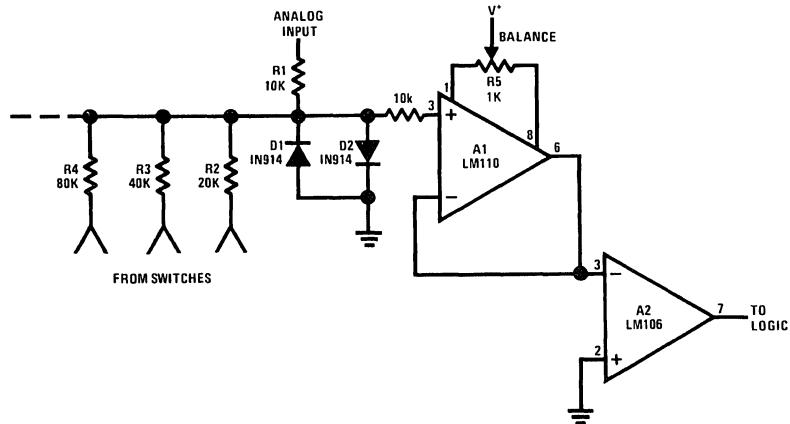
*Switch substrates are boot-strapped to reduce output capacitance of switch.



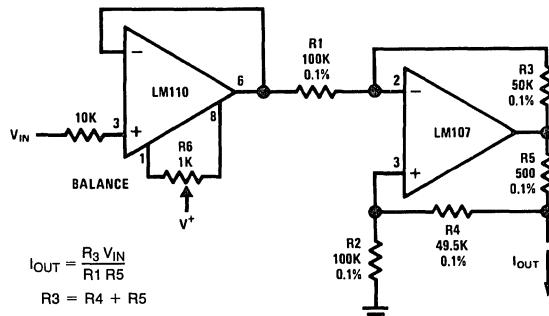
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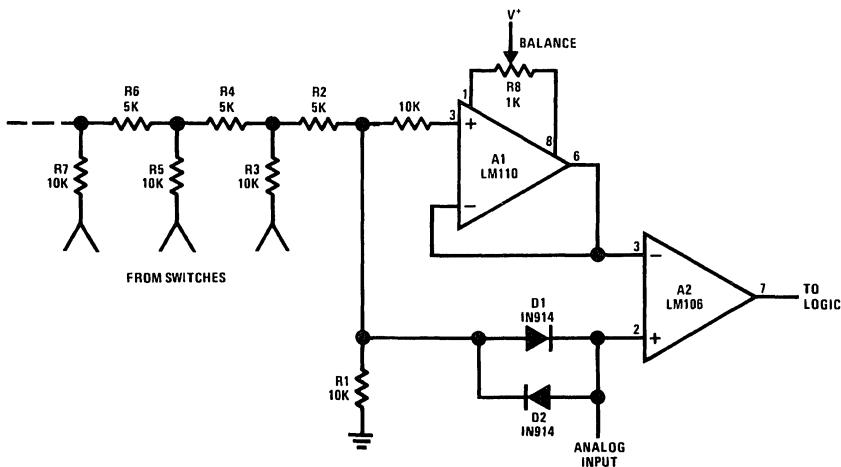
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Typical Applications (Continued)**Comparator for A/D Converter Using a Binary-Weighted Network**

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Bilateral Current Source

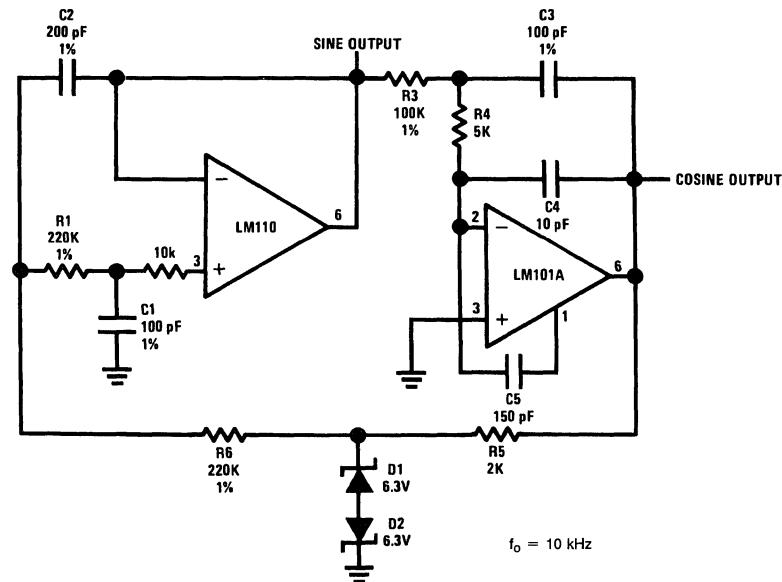
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Comparator for A/D Converter Using a Ladder Network

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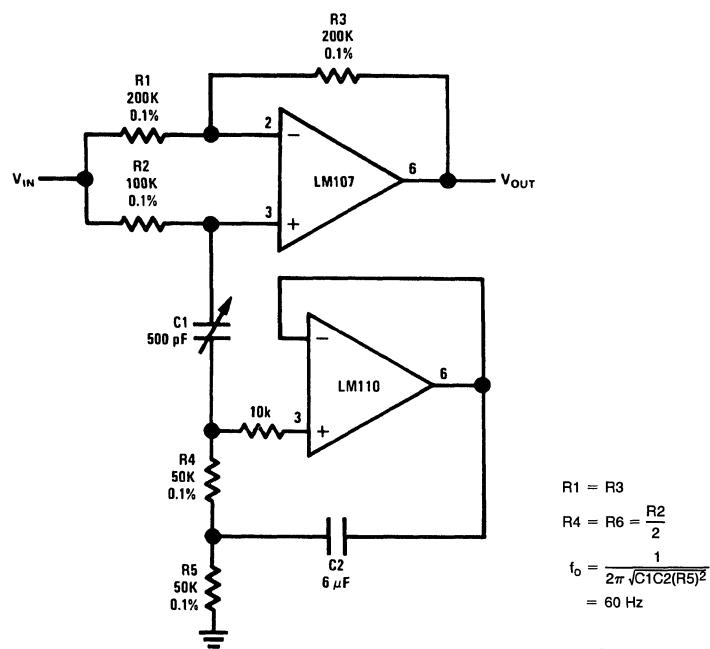
Typical Applications (Continued)

Sine Wave Oscillator

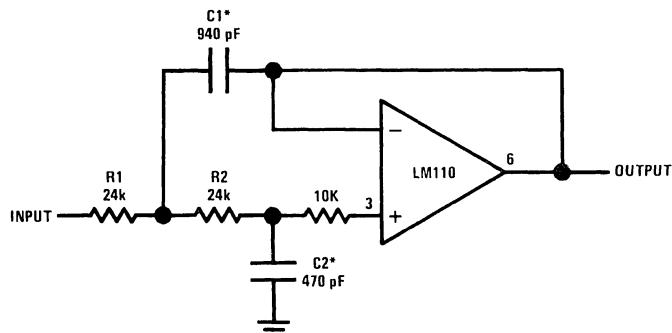


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Tunable Notch Filter

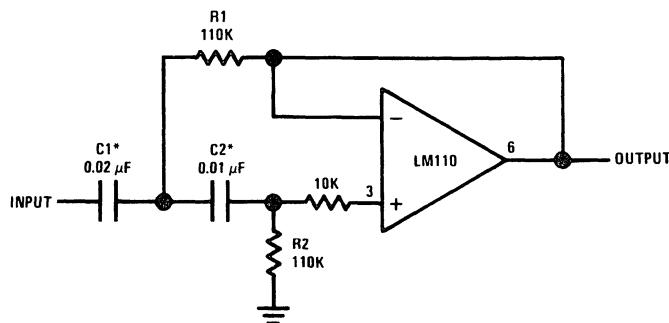


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Typical Applications (Continued)**Low Pass Active Filter**

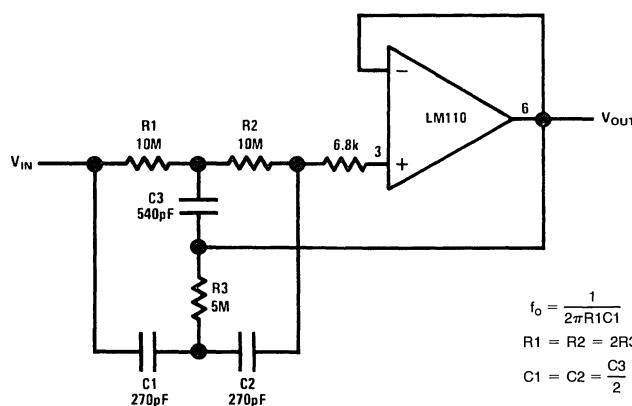
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*Values are for 10 kHz cutoff. Use silvered mica capacitors for good temperature stability.

High Pass Active Filter

TL/H/7761-19

*Values are for 100 Hz cutoff. Use metalized polycarbonate capacitors for good temperature stability.

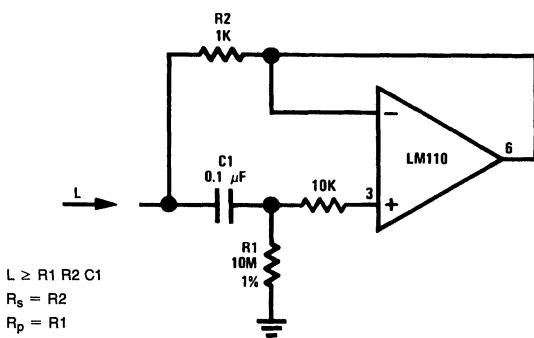
High Q Notch Filter

$$f_o = \frac{1}{2\pi R_1 C_1}$$

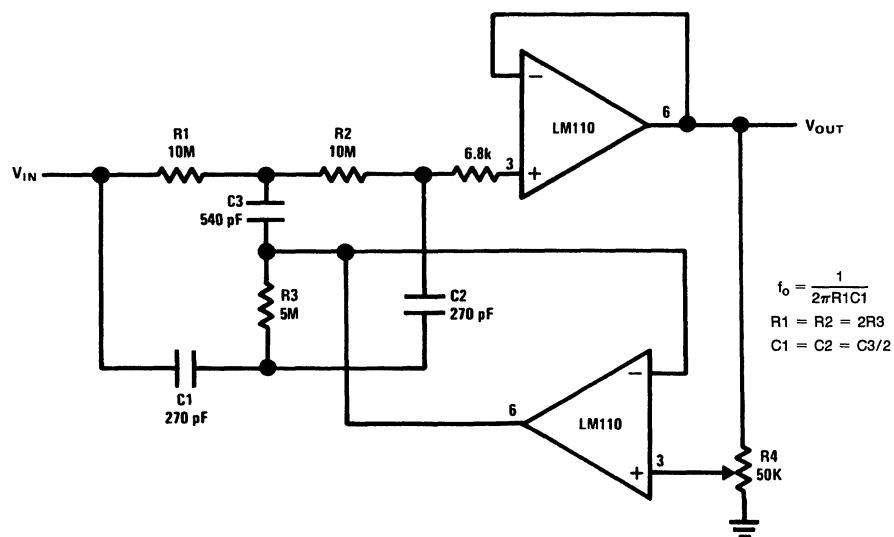
$$R_1 = R_2 = 2R_3$$

$$C_1 = C_2 = \frac{C_3}{2}$$

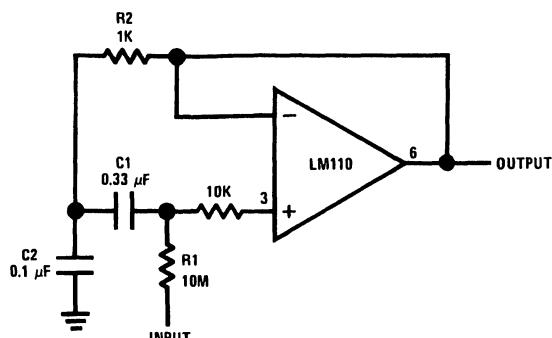
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Typical Applications (Continued)**Simulated Inductor**

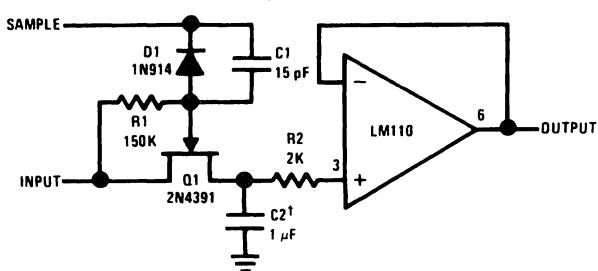
TL/H/7761-21

Adjustable Q Notch Filter

TL/H/7761-22

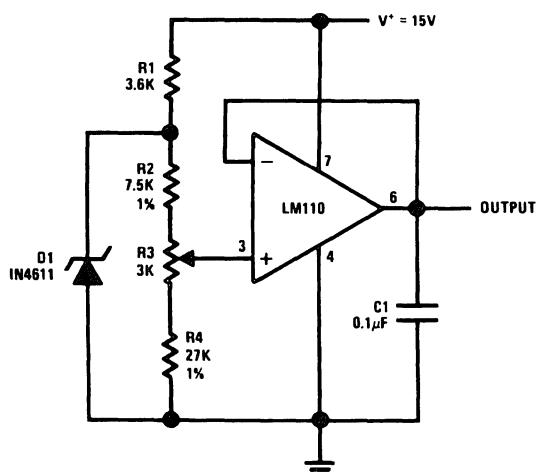
Bandpass Filter

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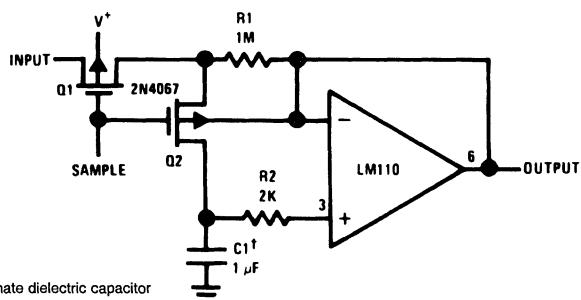
Typical Applications (Continued)**Sample and Hold**

TL/H/7761-24

†Use capacitor with polycarbonate teflon or polyethylene dietetric

Buffered Reference Source

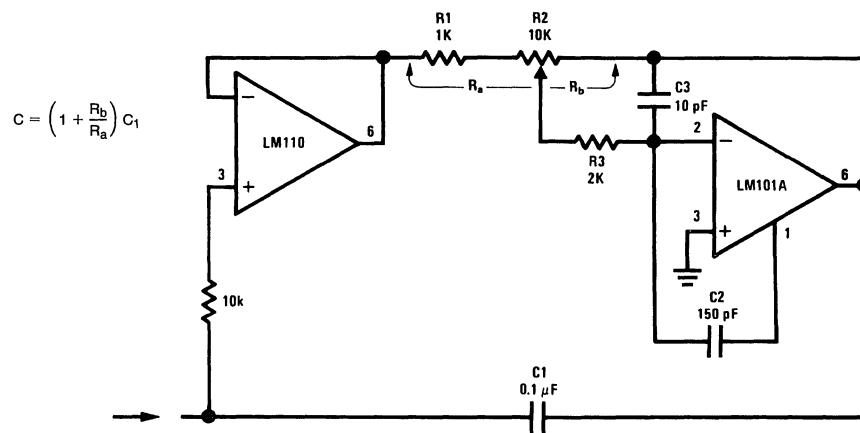
TL/H/7761-25

Typical Applications (Continued)**Low Drift Sample and Hold***

†Teflon polyethylene or polycarbonate dielectric capacitor

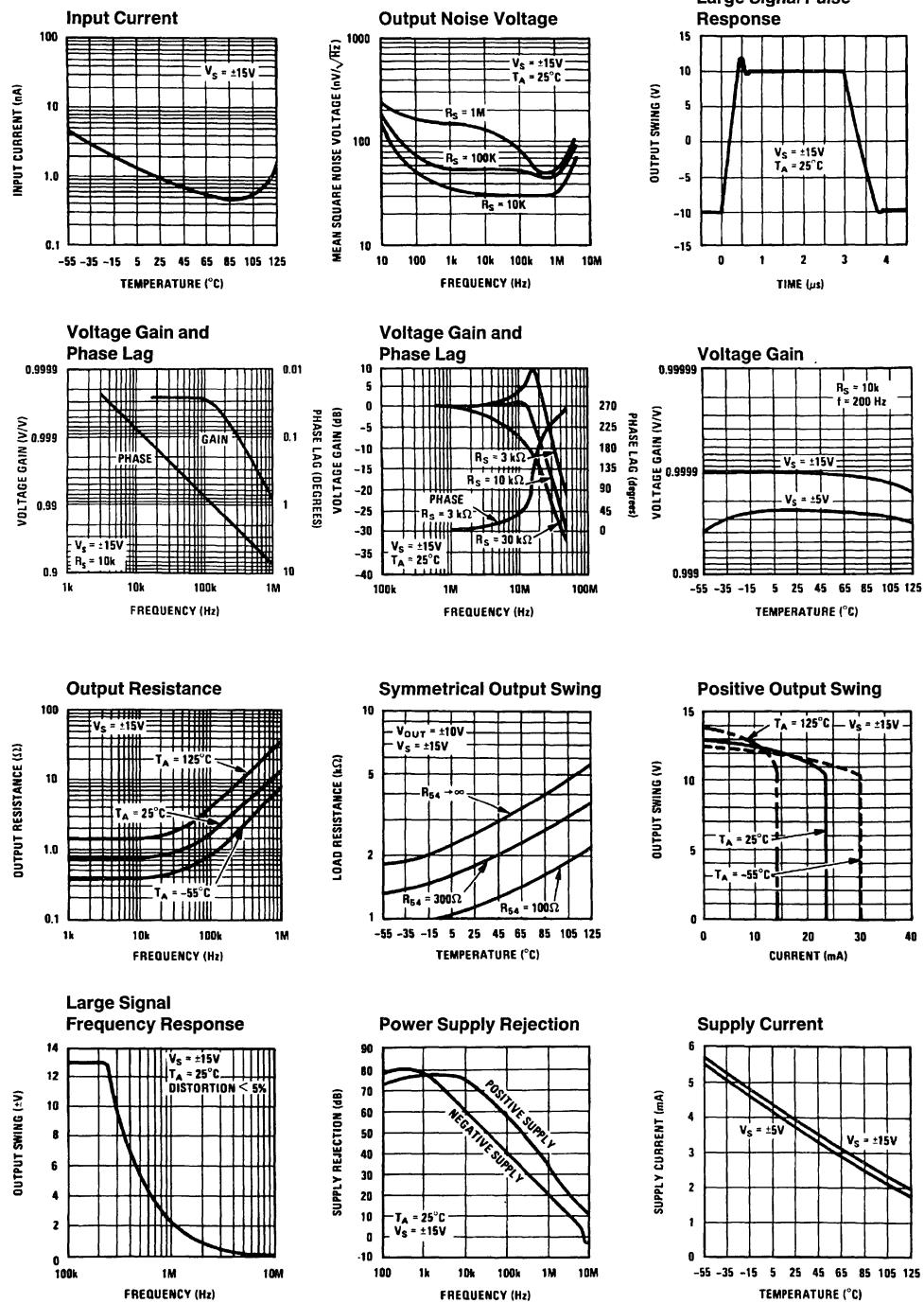
*Worst case drift less than 3 mV/sec

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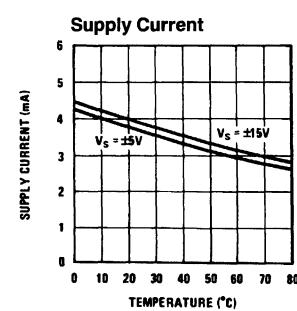
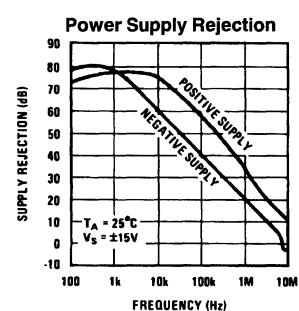
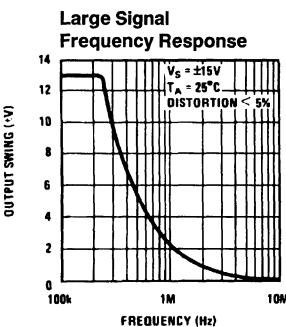
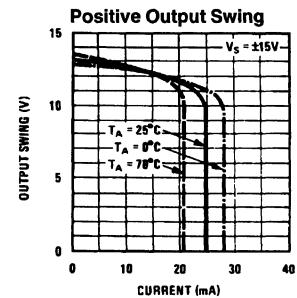
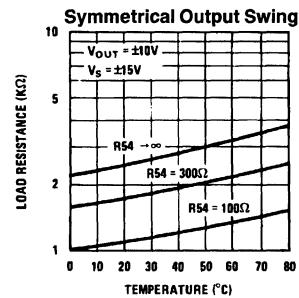
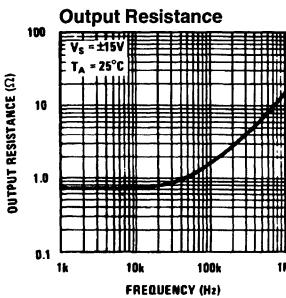
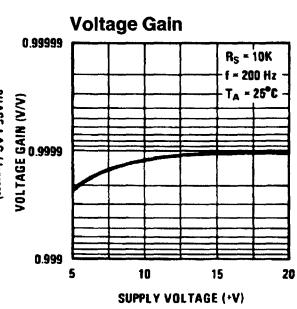
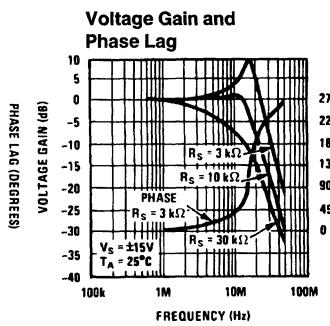
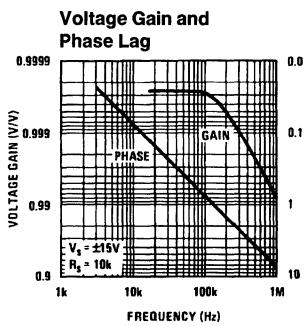
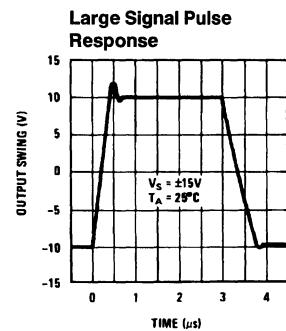
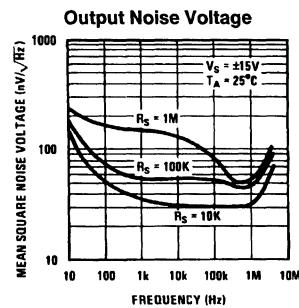
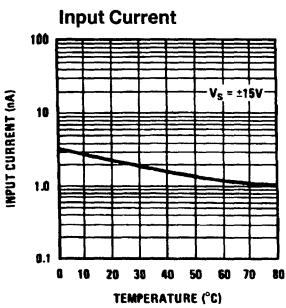
Variable Capacitance Multiplier

TL/H/7761-27

Typical Performance Characteristics (LM110/LM210)

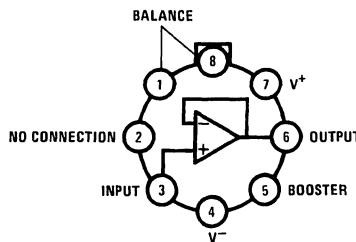


Typical Performance Characteristics (LM310)



Connection Diagrams

Metal Can Package

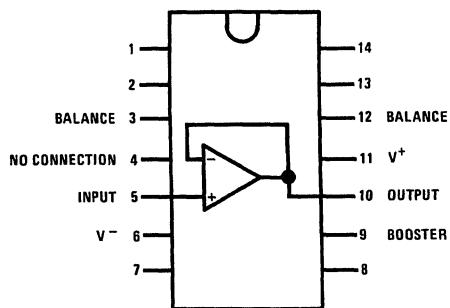


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Package is connected to Pin 4 (V^-)**Top View**

**Order Number LM110H, LM210H or LM310H
See NS Package Number H08C**

Dual-In-Line Package



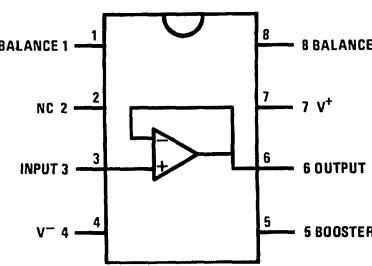
TL/H/7761-31

Note: Pin 6 connected to bottom of package.

Top View

**Order Number LM110J, LM210J or LM310J
See NS Package Number J14A**

Dual-In-Line Package



TL/H/7761-32

**Order Number LM310M or LM310N
See NS Package Number M08A or N08E**